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| L4 | 1458 | 711/103.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/18 18:14 |
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| L6 | 1950 | 711/154.ccls. | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/18 18:14 |
| L7 | 4256 | 4 or 5 or 6 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/18 18:14 |
| L8 | 19 | 1 and 3 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/18 18:15 |
| L10 | 4 | 7 and 8 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/18 18:16 |
| L11 | 643123 | offset | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/18 18:17 |
| L12 | 884 | 3 with 11 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/18 18:17 |
| L13 | 1 | 1 and 12 | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/18 18:17 |

| | | | | | | |
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| L14 | 24443758 | @ad<"20030909" | US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB | OR | ON | 2005/09/18 18:18 |
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IEE CNF IEE Conference Proceeding
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- ☐ 1. **Flexible use of memory for replication/migration in cache-coherent DSM multiprocessors**
Soundararajan, V.; Heinrich, M.; Verghese, B.; Gharachorloo, K.; Gupta, A.; Hennessy, J.;
Computer Architecture, 1998. Proceedings. The 25th Annual International Symposium on
27 June-1 July 1998 Page(s):342 - 355
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